00nT80"25h6E960

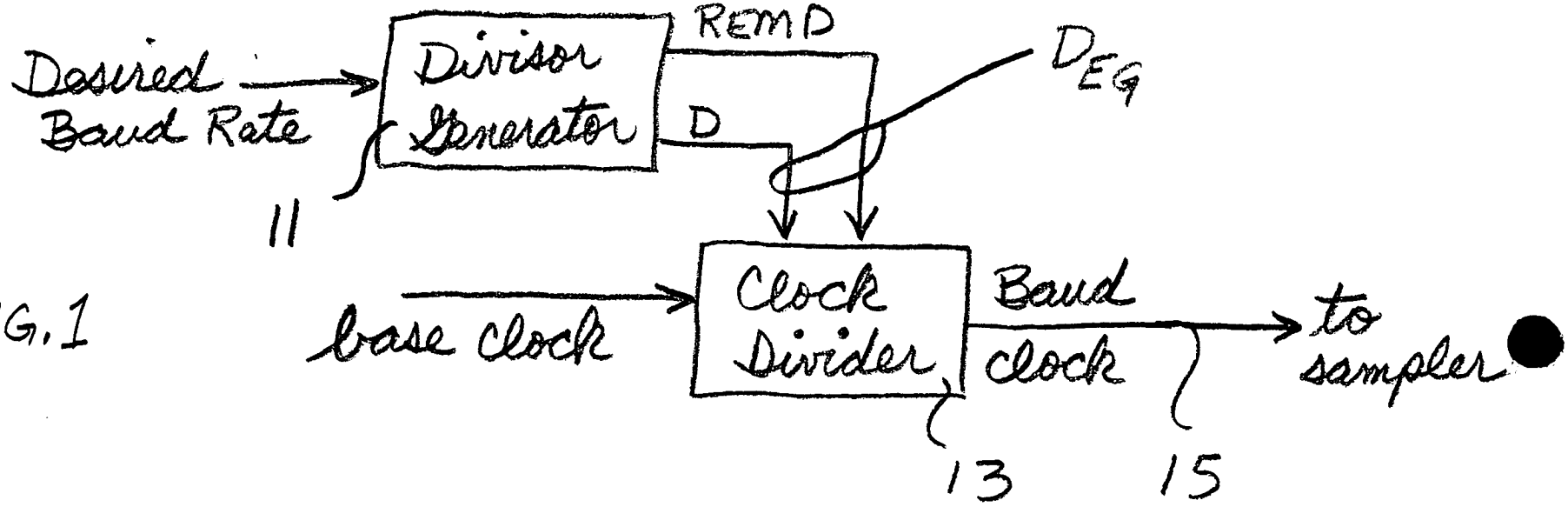

FIG. 1

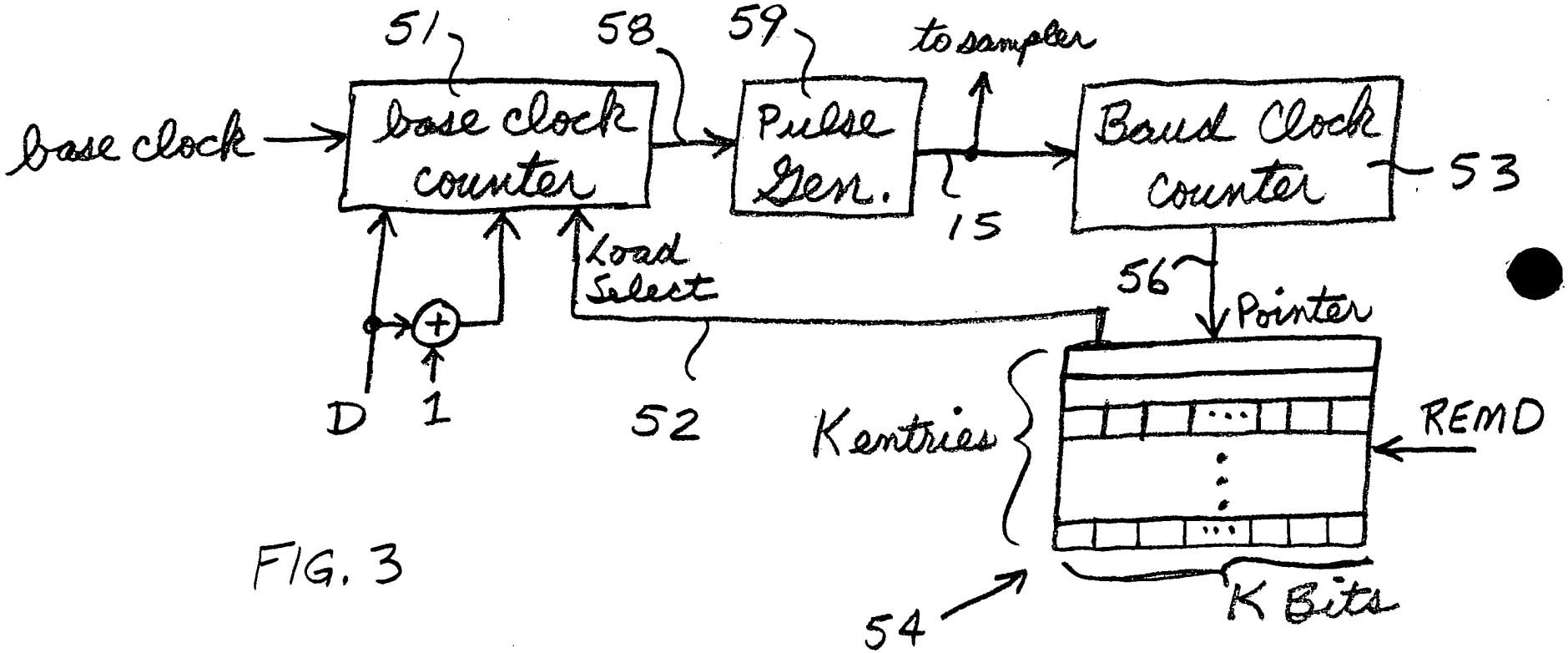

FIG. 3

0 0 1 6 0 " 2 6 6 9 6 0

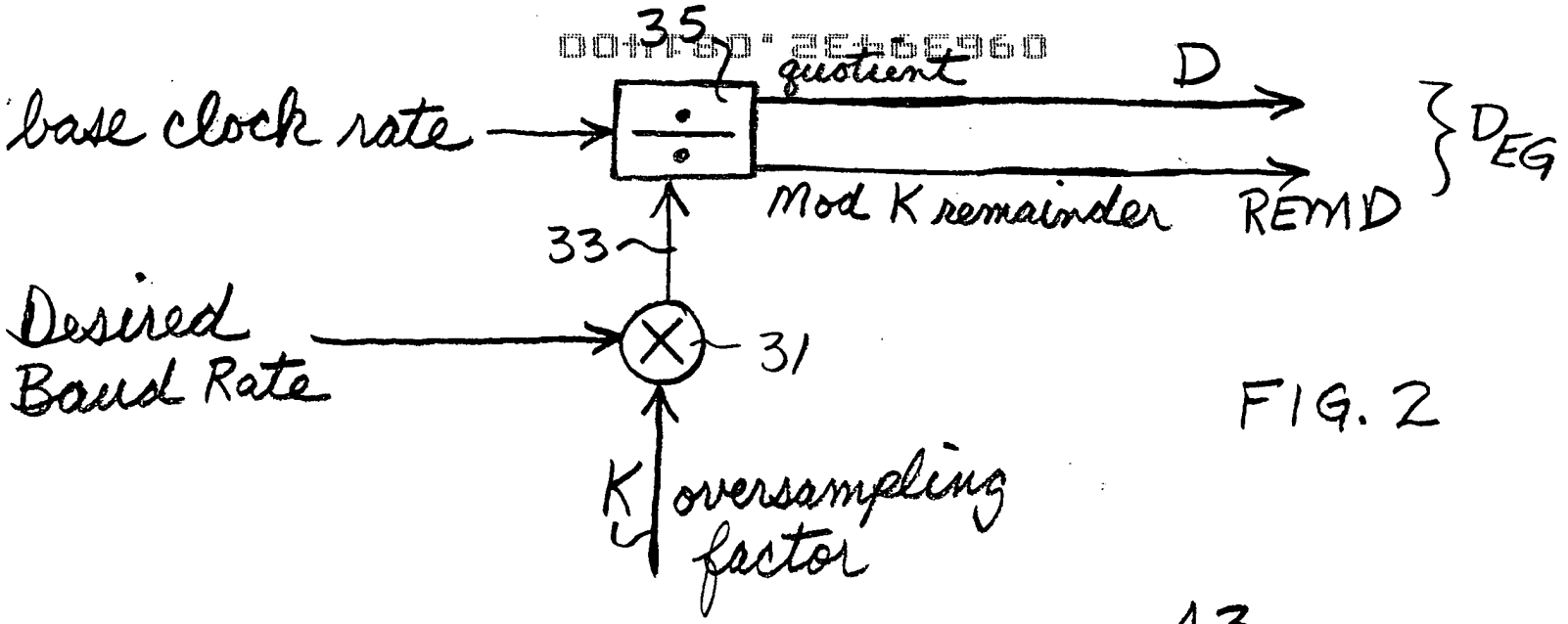

FIG. 2

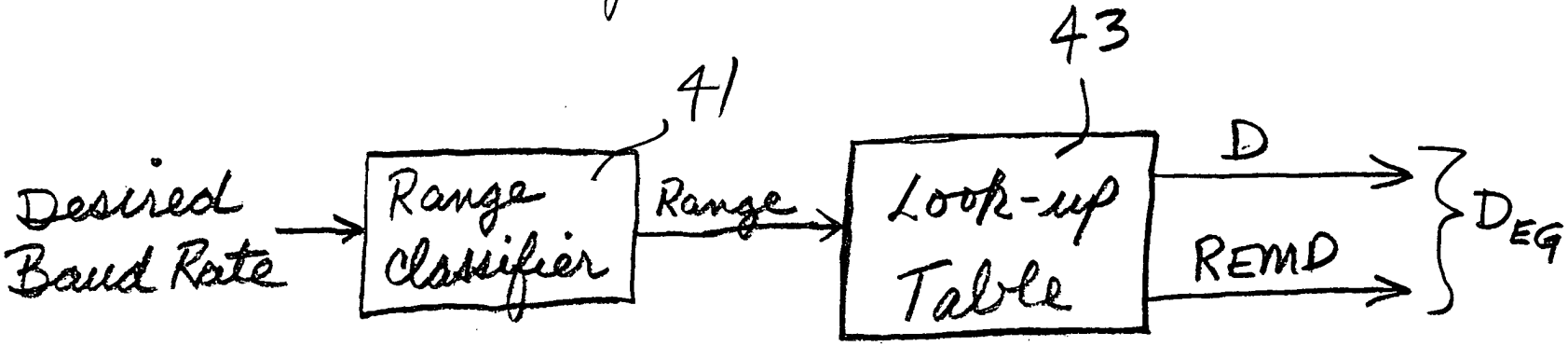

FIG. 7

REMD	
0 →	0000000000000000
1 →	0001000000000000
2 →	0001000000010000
3 →	0101000000010000
4 →	0101000000010100
5 →	0101010000010100
6 →	0101010000010101
7 →	0101010100010101
8 →	0101010101010101
9 →	0101011101010101
10 →	0101011101010111
11 →	0111011101010111
12 →	0111011101110111
13 →	0111111101110111
14 →	0111111111110111
15 →	1111111111110111

FIG. 4

0094500 "0295490

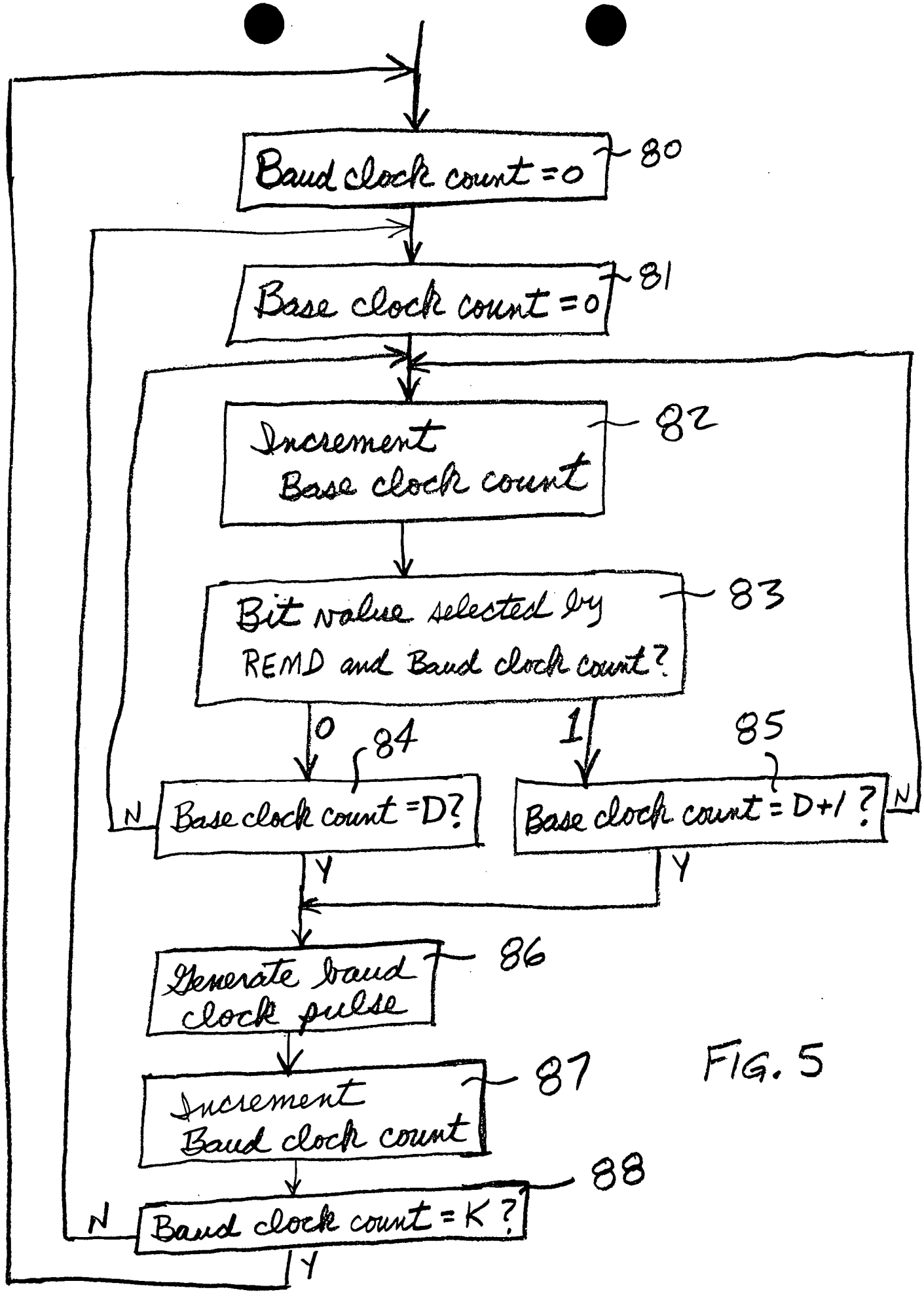
Baud clock count = 0
80
Base clock count = 0
81
Increment Base clock count
82
Bit value selected by REMD and Baud clock count?
83
0
84
1
85
Base clock count = D?
N
Y
Base clock count = D+1 ?
N
Y
Generate baud clock pulse
86
Increment Baud clock count
87
88
Baud clock count = K ?
N
Y

FIG. 5

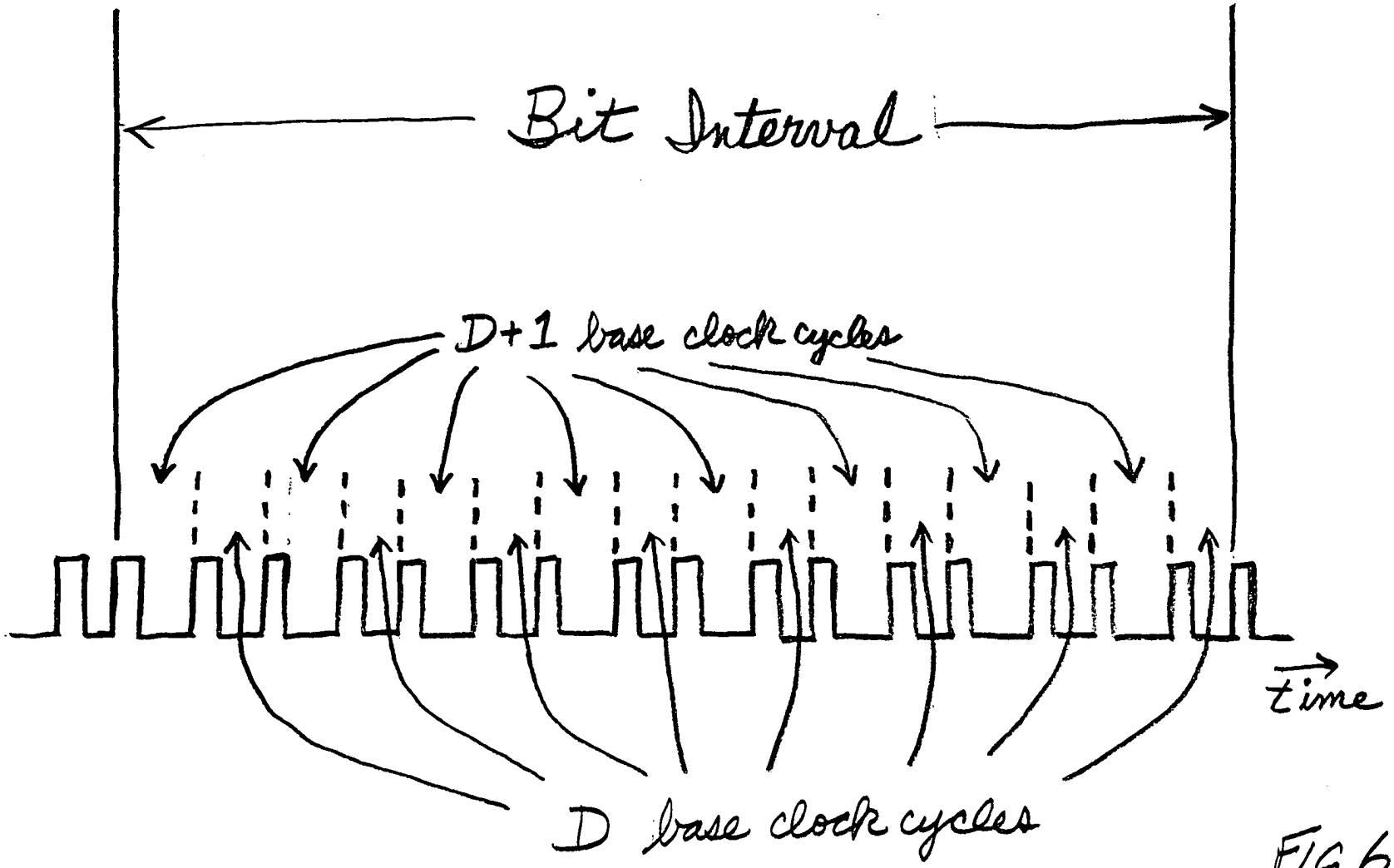

FIG. 6

Baud Rate Ranges	D	REMD
Range 1	1	0
Range 2	1	1
Range 3	1	2
⋮	↓	⋮
Range K	1	K-1
Range K+1	2	0
Range K+2	2	1
Range K+3	2	2
⋮	↓	⋮
Range 2K	2	K-1
Range 2K+1	3	0
Range 2K+2	3	1
Range 2K+3	3	2
⋮	⋮	⋮

FIG. 8

0054908.0054908

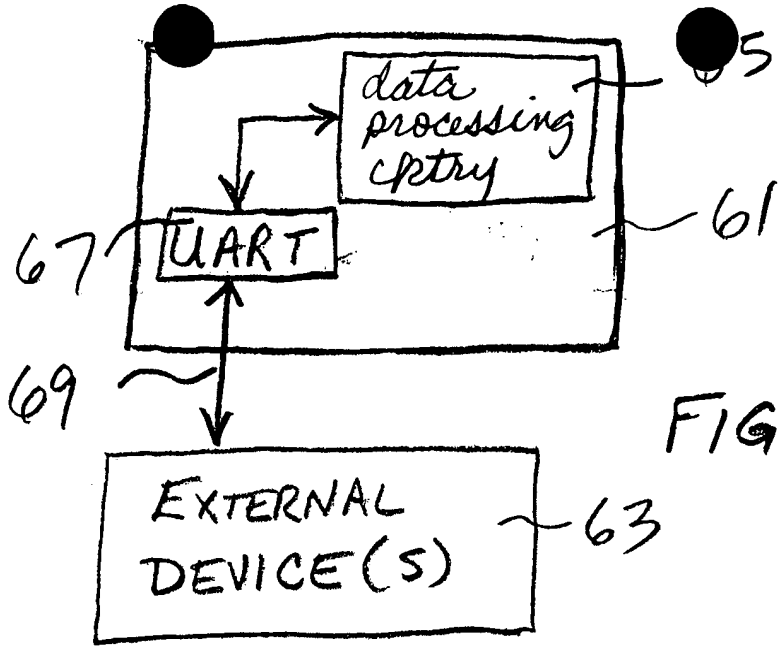

FIG. 9

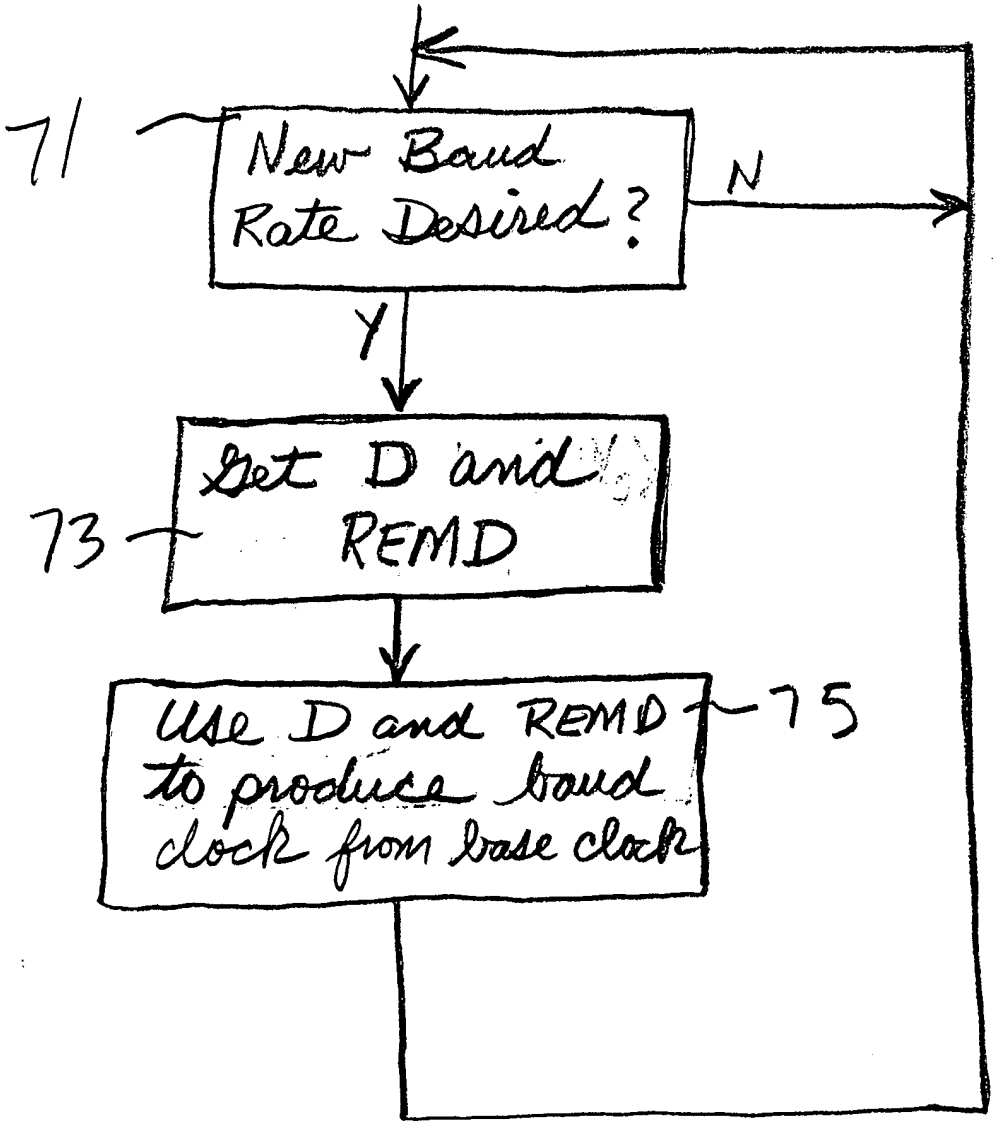

FIG. 10

0084400-0084500

00hT80" 2Eh6E960

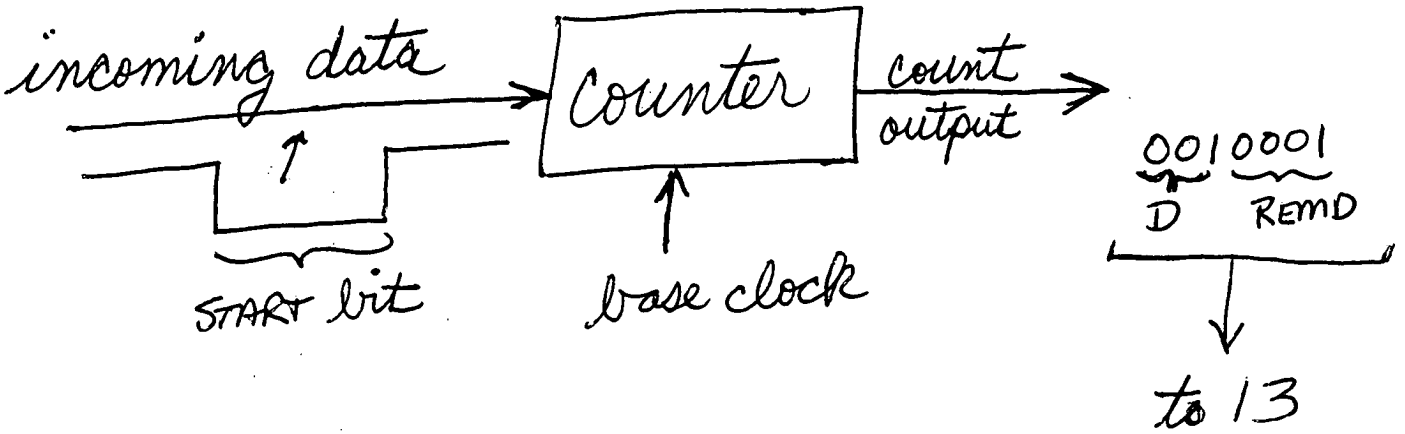

FIG. 11